



Docket No.: 42390P11446

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

RICHARD COULSON

Application No.: 09/945,266

Filed: August 31, 2001

For: **HARDWARE UPDATED METADATA
FOR NON-VOLATILE MASS STORAGE
CACHE**

Art Group: 2186

Examiner: W. Choi

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

02/22/2005 CCHAU1 00000109 022666 09945266

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The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Respectfully submitted,

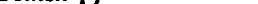
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: February 16, 2005

Gregory D. Caldwell, Reg. No. 39,926

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<p>REMARKS Substitute for form 1449A/PTO</p> <h1 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h1> <p style="text-align: center;"><i>(use as many sheets as necessary)</i></p>				<i>Complete if Known</i>	
Sheet	1	of		Application Number	09/945,266
				Filing Date	August 31, 2001
				First Named Inventor	Richard Coulson
				Art Unit	2186
				Examiner Name	W. Choi
				Attorney Docket Number	42390P11446

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US-4430712	02-07-1984	COULSON et al	
		US-4468730	08-28-1984	DODD et al	
		US-4503501	03-05-1985	COULSON et al	
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		US-5636355	06-03-1997	RAMAKRISHNAN et al	
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		US-2002/0160116	10-31-2002	NORDAL et al	
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		US-6263405		IRIE et al	
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		US-5442752	08-15-1995	STYCZINSKI	
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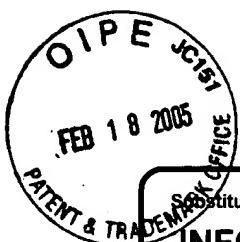
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		Number - Kind Code ² (if known)			
		US-3353430	11-21-1967	BRACKMAN et al	
		US-4908793	03-13-1990	YAMAGATA et al	
		US-4972364	11-20-1990	BARRETT et al	
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FOREIGN PATENT DOCUMENTS

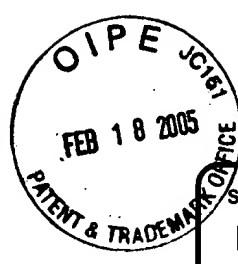
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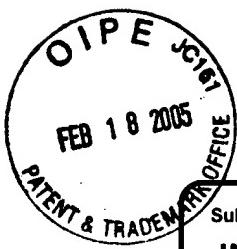
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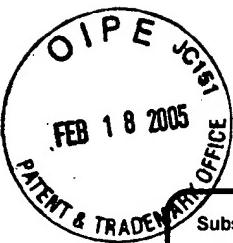
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		COULSON - US PATENT APPLICATION 09/602,008 "IN-LINE CACHE"	
		COULSON - US PATENT APPLICATION 09/602,009 "NON-VOLATILE CACHE EXPANSION BOARD"	
		COULSON - US PATENT APPLICATION 09/602,010 "NON-VOLATILE CACHE"	
		COULSON - US PATENT APPLICATION 09/602,011 "NON-VOLATILE CACHE INTEGRATED WITH MASS STORAGE DEVICE"	
		COULSON - US PATENT APPLICATION 10/740,633 "NON-VOLATILE MASS STORAGE CACHE COHERENCY APPARATUS ADMINISTRATOR"	
		AUCIELLO et al - THE PHYSICS OF FERROELECTRIC MEMORIES, Physics Today, July 1998, pp 22-27	
		SGS THOMSON - GSF32-16x16/90 32 MByte SIMM Glash Memory Module 4 pages	
		DERWENT - DOCUMENT ID RD 431135 A - "A raid controller with a removable NVRAM cache on detecting a battery or an NVRAM fault automatically flushes all...3 pages	
		DEROWITSCH - Smart Computing - "The Quiet Role of Chipsets" Aug. 1998, Vol 9, Issue 8	
		DIEFENDORFF - Intel Tries Integrating Graphics - In-Star MDR Aug. 24, 1998	
		DPT - Smart IV PM2144W PCI High Performance SCSI Adapter	
		DPT - News Release - DPT's SmartCache IV RAID/Caching Kits Outperform Adaptec's SCSI Adapters in Independent Tests and Reviews - Aug. 1, 1996	
		HODGES et al - 1983, pp 388-389 - Analysis and Design of Digital Integrated Circuits	
		INTEL - June 1997 - Intel 430HX PCIset Design Guide pp 1-1 thru 6-15	

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		IRE TRANSACTIONS - COMPONENT PARTS - Switching the Memory Matrix	
		MERZ - Sept. 1955 - Ferroelectric Storage Devices pp 335-342	
		MICROSOFT PRESS - Computer Dictionary 2nd Ed. 1994 - Nondestructive readout, page 270	
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		MUELLER - 2002 - UPGRADING AND REPAIRING PCS - pages 363-366 Chapter 9 - Chipset Evolution	
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		RAMTRON Dec. 7, 1999 - FM24C64 64Kb FRAM Serial Memory - 15 pages	
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		ROSENBERG - 1987 - Dictionary of Computers, Information Processing & Telecommunications, 2nd Ed. - P. 221	

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